DECLATION FOR PATENT APPOCATION

As a below named inventor, I declare: that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below) of the invention in

· LATERAL HIGH-BREAKDOWN-VOLTAGE TRANSISTOR

application on which priority is claimed:

the specification of which is attached hereto unless the following box is checked.

was filed on <u>December 26, 2000</u> as United States Application or PCT International Application No. 09/746,223 and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the

Country	Category	Application No.	Filing Date	Priority <u>Claim</u>
Japan	Patent	11-371568	December 27, 1999	Yes
Japan	Patent	2000-205070	July 6, 2000	Yes

DECLARATION FOR PATENT APPLICATION

I declare further that my post office address is at c/o Intellectual Property Division, KABUSHIKI KAISHA TOSHIBA. 1-1 Shibaura 1-chome. Minato-ku. Tokyo 105-8001, Japan; and that my citizenship and residence are as stated below next to my name:

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